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Request	Application Number	10/007,082		
for	Filing Date	December 6	5, 2001	
Continued Examination (RCE) Transmittal Address to: Mail Stop RCE	First Named Inventor	Minnick, et	al.	
	Art Unit	2151		
Commissioner for Patents P.O. Box 1450	Examiner Name	Madamba,	G.	
Alexandria, VA 22313-1450	Attorney Docket Number	P12249		
This is a Request for Continued Examination (RCE) under 37 CFR 1.114 of the above-identified application. Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8, 1995, or to any design application. See Instruction Sheet for RCEs (not to be submitted to the USPTO) on page 2.				
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Our Docket No.: P12249

Utility Patent Application

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of:)
Minnick, et al.)
Serial No.: 10/007,082	Examiner: Madamba, G.
Assignee: Intel Corporation) }
Filed: December 6, 2001	<i>)</i>)) Art Unit: 2151
For: METHOD AND APPARATUS FOR	j
PROCESSING LATENCY SENSITIVE)
ELECTRONIC DATA WITH INTERRUPT)
MODERATION	1

RCE SUBMISSION UNDER 37 CFR §1.114(c)

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